

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Reif et al.
Serial No. : 10/749,096
Filed : December 30, 2003
Title : MULT-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE HAVING
AN ELECTRICAL SHIELDING PORTION

Art Unit : 2811
Examiner : Owens, Douglas W.

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


PETITION FOR A ONE-MONTH EXTENSION OF TIME

Pursuant to 37 CFR §1.136, Applicants hereby request that the period for response to the action dated August 1, 2005, be extended for one month to and including December 1, 2005. Enclosed is a check for \$110.00. No other fee is believed to be due for this Extension however, if any other fees are due, please apply such fees to Deposit Account No. 50-0845 referencing Attorney Docket: MIT-136DUS.

Respectfully submitted,

Date:

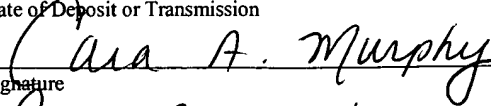
3 November 2005


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CERTIFICATE OF MAILING OR TRANSMISSION (37 C.F.R. §1.8(a))

I hereby certify that this correspondence is being transmitted via facsimile to Commissioner of Patents or deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to: MS RCE, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on the date set forth below.

3- November - 2005
Date of Deposit or Transmission

Signature
Cara A. Murphy
Typed or Printed Name of Person Signing Certificate

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02 FC:1251 30.00 DA 90.00 DP